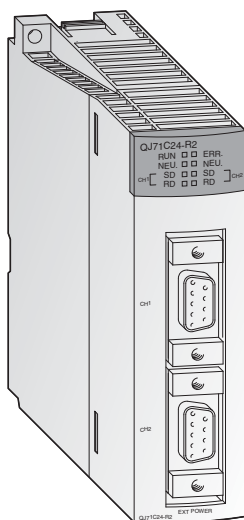


Interface Modules



Data exchange with peripheral devices

This module enables communication with peripheral devices via a standard RS232 interface. The peripherals are connected point-to-point on a 1:1 basis.

Special features:

- The QJ71C24N provides one RS232 and one RS422/485 interface. The QJ71C24-R2 provides two RS232 interfaces and the QJ71C24N-R4 two RS422/485 interfaces.
- Enables PCs connected to the system to access the full data set of the MELSEC System Q CPU using graphic process supervision or monitoring software
- Integrated flash ROM memory for logging quality, productivity or alarm data that can be printed out when required
- Module and communications status shown by LEDs
- Communications test and monitor function are possible with the software GX-Configurator UT

Specifications		QJ71C24N	QJ71C24N-R2	QJ71C24N-R4	QJ71MB91	
Interface	channel 1	RS232 (9-pin Sub-D)	RS232 (9-pin Sub-D)	RS422/RS485 (screw terminals)	RS232 (9-pin Sub-D)	
	channel 2	RS422/RS485 (screw terminals)	RS232 (9-pin Sub-D)	RS422/RS485 (screw terminals)	RS422/RS485 (screw terminals)	
Communications mode		Full duplex/half duplex	Full duplex/half duplex	Full duplex/half duplex	Full duplex/half duplex	
Synchronisation		Asynchronous communications	Asynchronous communications	Asynchronous communications	Master/Slave	
Data transfer	Rate	50–230400 (channel 1 only) 115200 (channel 1+2 simultaneously)	50–230400 (nur Kanal 1) 115200 (channel 1+2 simultaneously)	50–230400 (channel 1 only) 115200 (channel 1+2 simultaneously)	300–115200	
	Distance RS232	m	15	—	15	
	Distance RS422/485	m	1200 (if both channels are used)	—	1200 (if both channels are used)	1200
Max. no of stations in a multidrop network		No restrictions/64	—	no restrictions/64	Master (32 slaves) Slave (242)	
Data format		1 start bit, 7 or 8 data bits, 1 or 0 parity bits, 1 or 2 stop bits	1 start bit, 7 or 8 data bits, 1 or 0 parity bits, 1 or 2 stop bits	1 start bit, 7 or 8 data bits, 1 or 0 parity bits, 1 or 2 stop bits	Modbus RTU	
Error correction		Parity check, checksum	Parity check, checksum	Parity check, checksum	—	
DTR/DSR control		YES/NO selectable	YES/NO selectable	—	—	
X ON/X OFF (DC1/DC3)		YES/NO selectable	YES/NO selectable	YES/NO selectable	—	
I/O points		32	32	32	32	
Internal power consumption (5 V DC)		mA	310	260	390	310
Weight		kg	0.2	0.2	0.2	0.2
Dimensions (WxHxD)		mm	27.4x98x90	27.4x98x90	27.4x98x90	27.4x98x90
Order information		Art. no.	149500	149501	149502	167757